

Free Active Edge Stack (LIFO Stack Memory)

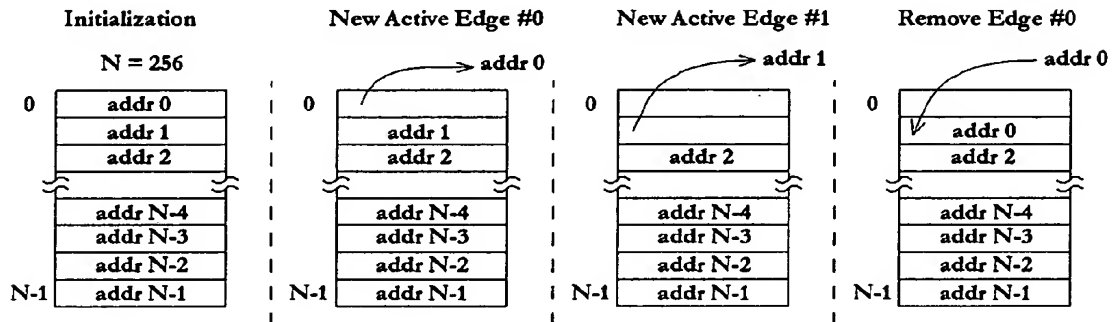


FIG. 5(a)

Active Edge Table: adding a new edge

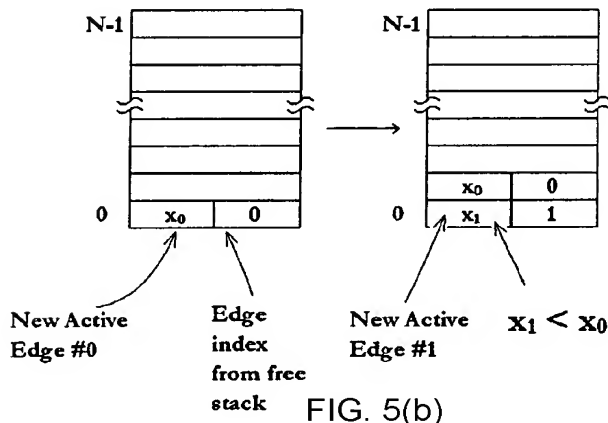


FIG. 5(b)

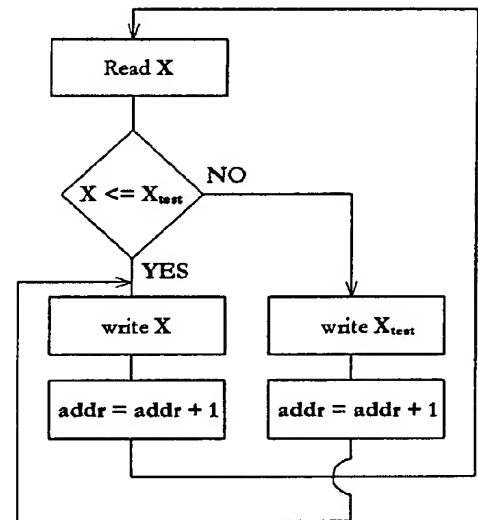


FIG. 5(c)

Active Edge Table: updating edge #3

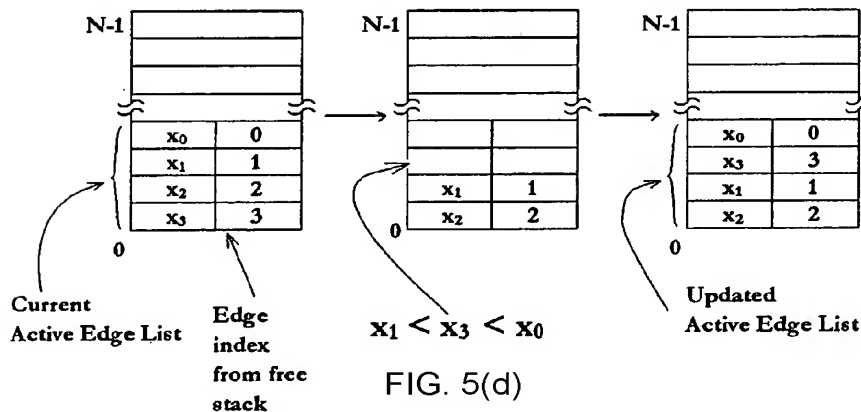
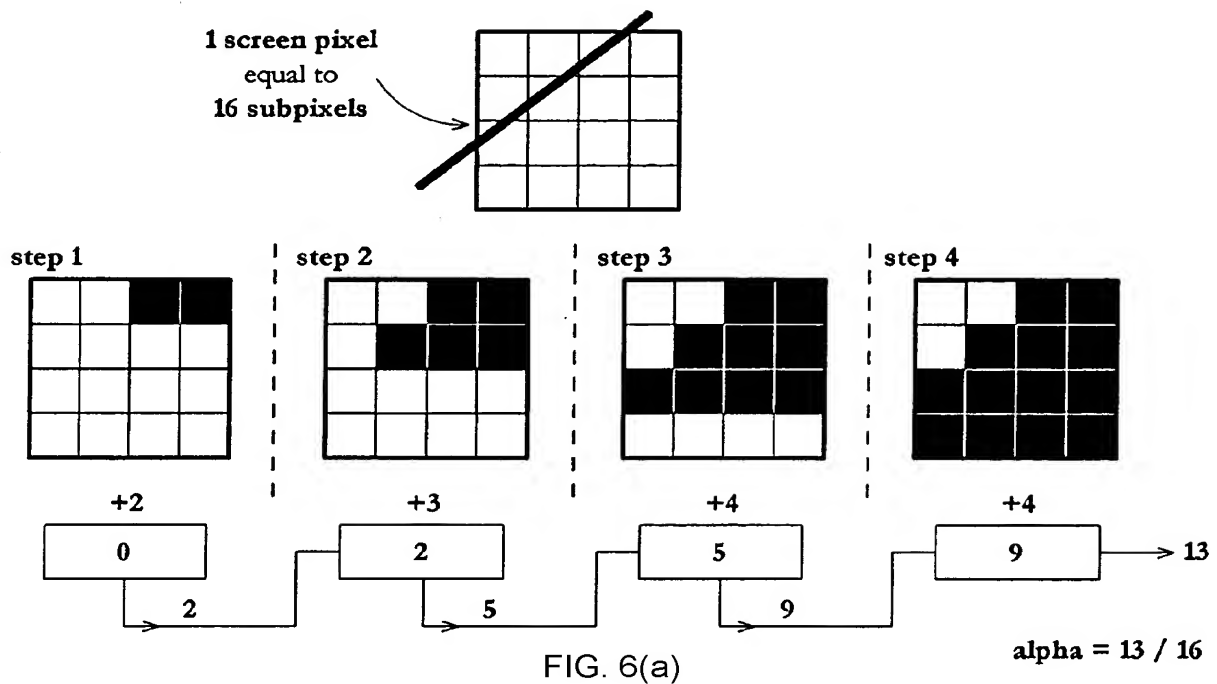
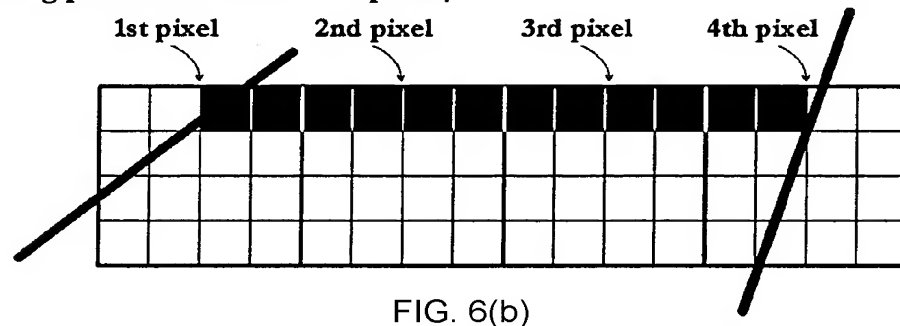


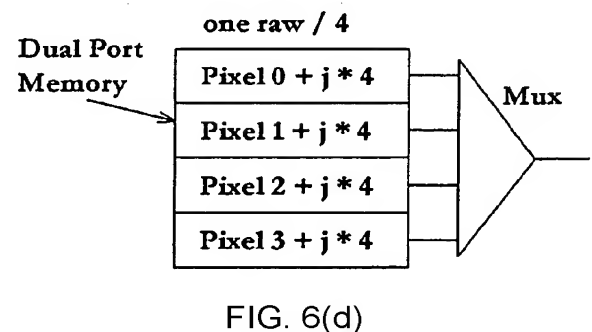
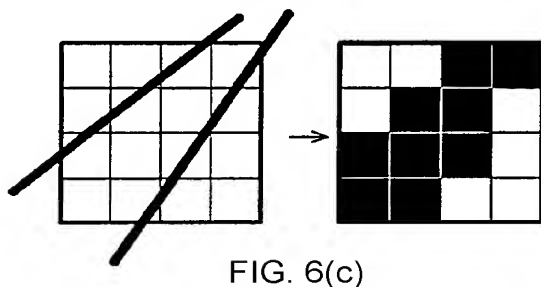
FIG. 5(d)



Anti aliasing parallelism with $N = 4$ pixels;



Init - End inside the same sub-pixel matrix



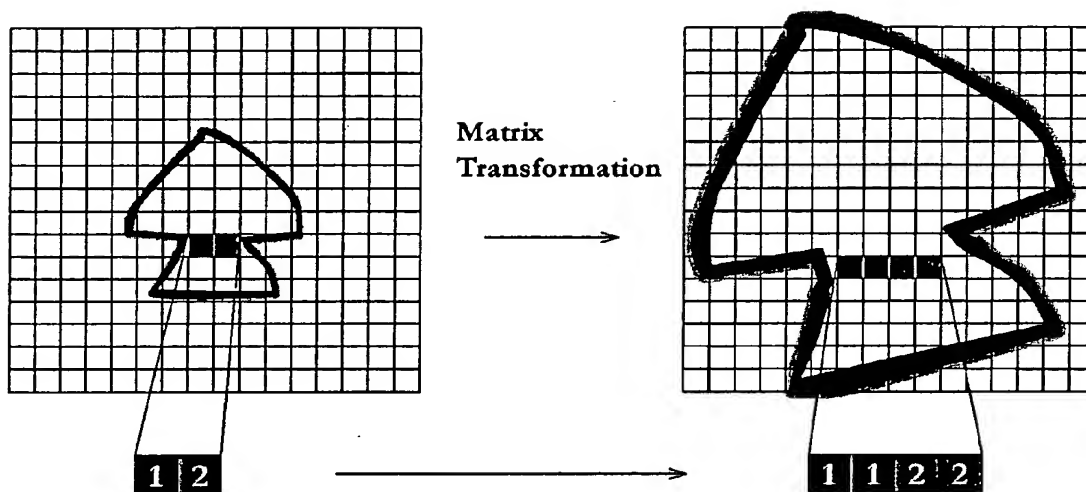


FIG. 7(a)

FIG. 7(b)

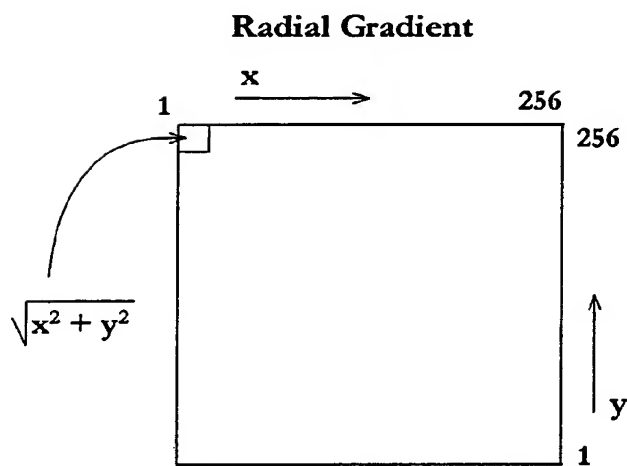


FIG. 7(c)

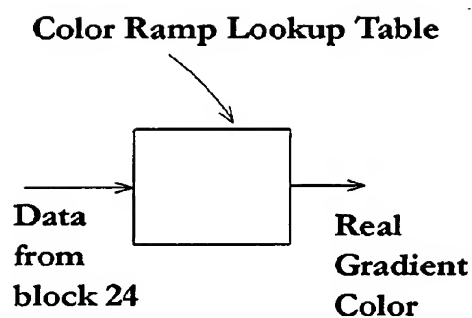


FIG. 7(d)

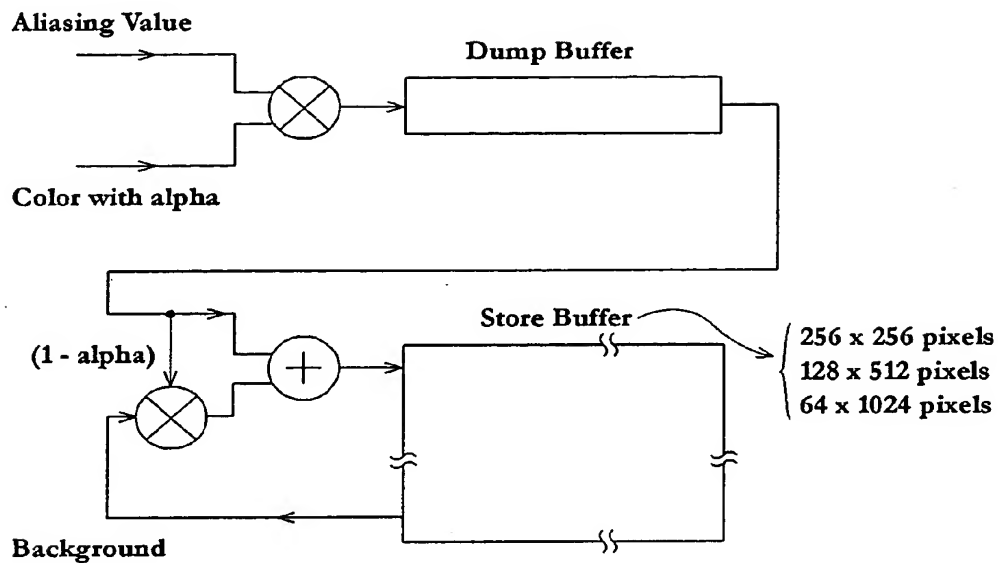


FIG. 8(a)

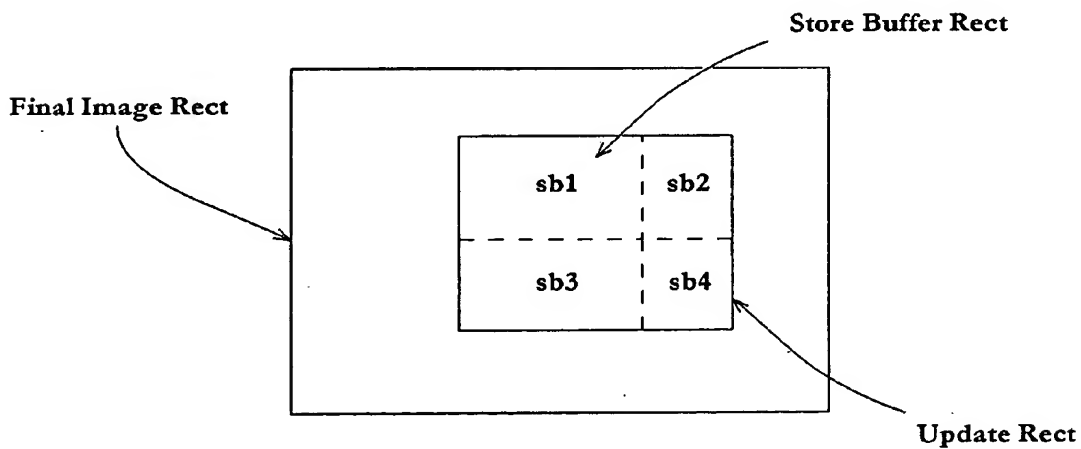


FIG. 8(b)